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L1 and ((???shift adj register) and (control\$4 adj logic))	1

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Search History

DATE: Tuesday, August 31, 2004 Printable Copy Create Case

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DB=PC	GPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=NO; OP=	OR	
<u>L7</u>	L1 and ((????shift adj register) and (control\$4 adj logic))	1	<u>L7</u>
<u>L6</u>	L1 and ((error adj detection adj3 test\$3))	2	<u>L6</u>
<u>L5</u>	L1 and ((preshift adj register) and (control\$4 adj logic))	1	<u>L5</u>
<u>L4</u>	L1 and (preshift adj register)	1	<u>L4</u>
<u>L3</u>	L1 and (serial adj parallel adj converter)	7	<u>L3</u>
<u>L2</u>	L1 and (serial adj parrallel adj converter)	0	<u>L2</u>
<u>L1</u>	(receiv\$3 adj signals) and (control\$4 adj light\$3) and computer	. 999	L1

END OF SEARCH HISTORY